

Listing of Claims

1. (Currently Amended) In a computing device that implements a video encoder, a method of encoding a video image in a video image sequence, wherein the video image is partitioned into sets of pixels, the method comprising:

with the computing device that implements the video encoder:

encoding a set of pixels, including:

determining a value for a switch code, wherein the value for the switch code indicates whether the set of pixels is intra-coded or inter-coded; and

jointly coding the value for the switch code with motion vector information for the set of pixels and with a terminal symbol indicating whether transform coefficient data is encoded for the set of pixels in a bit stream, wherein a single instance of a single variable length code represents a value combination for the value for the switch code, the motion vector information and the terminal symbol, the single instance of the single variable length code being selected from a variable length code table of different value combinations for the switch code, the motion vector information and the terminal symbol; and

outputting the single instance of the single variable length code in the bit stream.

2. (Original) The method of claim 1 wherein the set of pixels is a block.

3. (Original) The method of claim 1 wherein the set of pixels is a macroblock.

4. (Original) The method of claim 1 wherein the value for the switch code indicates the set of pixels is intra-coded, and wherein the motion vector information comprises a pseudo motion vector.

5. (Currently Amended) In a computing device that implements a video encoder, a method of encoding a video image in a video image sequence, wherein the video image is partitioned into sets of pixels, the method comprising:

with the computing device that implements the video encoder:

encoding a set of pixels, including:

determining a value for a switch code, wherein the value for the switch

code indicates whether the set of pixels is intra-coded or inter-coded;

jointly coding the value for the switch code with motion vector

information for the set of pixels and with a terminal symbol indicating whether transform coefficient data is encoded for the set of pixels, wherein the jointly coding yields an extended motion vector code that is a single instance of a single variable length code representing a value combination for (a) the value for the switch code, (b) the motion vector information and (c) the terminal symbol, the single instance of the single variable length code being selected from a variable length code table of different value combinations for the switch code, the motion vector information and the terminal symbol, wherein the terminal symbol indicates whether subsequent data for the set of pixels is to be output; and

outputting the single instance of the single variable length code in a bit stream.

6. (Original) The method of claim 5 further comprising jointly coding additional data for the set of pixels with the extended motion vector code.

7. (Original) The method of claim 5 wherein the video image is a bi-directionally predicted video image, further comprising jointly coding an index for a reference image for the predicted video image with the extended motion vector code.

8. (Original) The method of claim 5 wherein the video image is a field-coded video image, further comprising jointly coding an index for a reference field for the field-coded video image with the extended motion vector code.

9. (Original) The method of claim 5 further comprising jointly coding fading information for the video image with the extended motion vector code.

10. (Original) The method of claim 5 further comprising jointly coding an entropy code table index for the video image with the extended motion vector code.

11. (Original) The method of claim 5 wherein the set of pixels is a block.

12. (Original) The method of claim 5 wherein the set of pixels is a macroblock.

13- 47. (Canceled)

48. (Currently Amended) A method of reconstructing one or more video images in a video sequence using a computing device that implements a video decoder, the method comprising:

with the computing device that implements the video decoder:

receiving encoded data from a bit stream; and

decoding a set of pixels using the encoded data from the bit stream, wherein the decoding the set of pixels comprises:

decoding an extended motion vector code for the set of pixels, wherein the extended motion vector code reflects joint encoding of motion information together with intra/inter decision information indicating whether the set of pixels is intra-coded or inter-coded and with a terminal symbol, wherein the extended motion vector code is a single instance of a single variable length code representing a combination of values for (a) the intra/inter decision information, (b) the motion information and (c) the terminal symbol, and wherein the decoding the extended motion vector code uses a variable length code table of different value combinations for the intra/inter decision information, the motion information and the terminal symbol; and

determining whether transform coefficient data for the set of pixels is included in the bit stream based at least in part upon the terminal symbol.

49. (Previously Presented) The method of claim 48 wherein the extended motion vector code indicates the set of pixels is skip-coded.

50. (Previously Presented) The method of claim 48 wherein the set of pixels is intra-coded, and wherein the motion information comprises a pseudo motion vector.

51. (Previously Presented) The method of claim 48 wherein the motion information comprises motion vector information for a differential motion vector for the set of pixels.

52. (Previously Presented) The method of claim 48 wherein the extended motion vector code is preceded in the bit stream by header information.

53. (Previously Presented) The method of claim 48 wherein the extended motion vector code is followed in the bit stream by a coded block pattern code.

54. (Previously Presented) The method of claim 48 wherein the determining is based on the terminal symbol.

55. (Previously Presented) The method of claim 48 wherein the set of pixels is a macroblock.

56. (Previously Presented) The method of claim 55 further comprising decoding a second extended motion vector code for the macroblock.

57. (Previously Presented) The method of claim 56 wherein the macroblock is a bi-directionally predicted macroblock.

58. (Previously Presented) The method of claim 56 wherein the macroblock is a field-coded interlace macroblock.

59. (Previously Presented) The method of claim 55 further comprising decoding an extended motion vector code for each block in the macroblock.

60. (Previously Presented) The method of claim 59 wherein the extended motion vector codes are preceded in the bit stream by a modified coded block pattern code.

61-63. (Canceled)

64. (Previously Presented) The method of claim 55 wherein the macroblock includes

four blocks each comprising an 8x8 array of luminance pixels, and two blocks each comprising an 8x8 array of chrominance pixels.

65. (Previously Presented) The method of claim 55 wherein the macroblock includes four blocks each comprising an 8x8 array of luminance pixels, and four blocks each comprising a 4x8 array of chrominance pixels.

66. (Previously Presented) The method of claim 55 wherein the macroblock includes four blocks each comprising an 8x8 array of luminance pixels, and four blocks each comprising an 8x8 array of chrominance pixels.

67. (Canceled)

68. (Canceled)

69. (Currently Amended) A computer system that implements a video decoder, the system comprising:

~~one or more plural~~ processors;

memory;

~~at least one an~~ input device[[.]]; ~~an~~

~~an~~ output device; [[or]]

~~a~~ communication connection; and

one or more storage media storing instructions for causing the system to decode video using:

means for receiving encoded data from a bit stream, ~~wherein the means for receiving includes a jitter buffer;~~ and

means for decoding one or more video images in a video image sequence, wherein the one or more video images comprise sets of pixels, and wherein the means for decoding comprises:

means for decoding an extended motion vector code for a set of pixels, wherein the extended motion vector code reflects joint encoding of motion information together

with intra/inter decision information indicating whether the set of pixels is intra-coded or inter-coded and with a terminal symbol, wherein the extended motion vector code is a single instance of a single variable length code representing a combination of values for (a) the intra/inter decision information, (b) the motion information and (c) the terminal symbol, and wherein the decoding the extended motion vector code uses a variable length code table of different value combinations for the intra/inter decision information, the motion information and the terminal symbol; and

means for determining whether subsequent data for the set of pixels is included in the bit stream based at least in part upon the terminal symbol.

70. (Currently Amended) A computer system that implements a video encoder, the system comprising:

one or more plural processors;

memory;

an at least one input device[. . .];

an output device; [[or]]

a communication connection; and

one or more storage media storing instructions for causing the system to encode video using:

means for encoding one or more video images in a video image sequence, wherein the one or more video images comprise sets of pixels, and wherein the means for encoding comprises:

means for encoding an extended motion vector code for a set of pixels, wherein the extended motion vector code reflects joint encoding of motion information together with intra/inter decision information indicating whether the set of pixels is intra-coded or inter-coded and with a terminal symbol, wherein the terminal symbol indicates whether subsequent data for the set of pixels is included in a the needed bit stream, and wherein the extended motion vector code is a single instance of a single variable length code representing a combination of values for (a) the intra/inter decision information, (b) the motion information and (c) the terminal symbol, the single instance of the single variable length code being selected from a variable length code table of different value combinations for the intra/inter decision

information, the motion information and the terminal symbol; and

means for outputting encoded data including the single instance of the single variable length code in the bit stream.

71. (Currently Amended) A method of reconstructing one or more video images in a video sequence using a computing device that implements a video decoder, the method comprising:

with the computing device that implements the video decoder:

receiving encoded data from a bit stream; and

decoding a macroblock using the encoded data from the bit stream, wherein the decoding the macroblock comprises:

decoding an extended motion vector code for the macroblock, wherein the extended motion vector code reflects joint encoding of motion vector information together with intra/inter decision information indicating whether the macroblock is intra-coded or inter-coded and with a terminal symbol, wherein the extended motion vector code is a single instance of a single variable length code representing a combination of values for (a) the intra/inter decision information, (b) the motion vector information and (c) the terminal symbol, and wherein the decoding the extended motion vector code uses a variable length code table of different value combinations for the intra/inter decision information, the motion vector information and the terminal symbol;

determining whether subsequent transform coefficient data for the macroblock is included in the bit stream based at least in part upon the terminal symbol; and

if the terminal symbol indicates subsequent transform coefficient data for the macroblock is included in the bit stream, decoding a coded block pattern code for the macroblock, and otherwise skipping the decoding of the coded block pattern code for the macroblock, wherein the terminal symbol indicates whether the coded block pattern code for the macroblock is included in the bit stream.

72. (Previously Presented) The method of claim 71 wherein the extended motion vector code indicates the set of pixels is skip-coded.

73. (Previously Presented) The method of claim 71 wherein the set of pixels is intra-coded, and wherein the motion vector information comprises a pseudo motion vector.

74. (Previously Presented) The method of claim 71 wherein the motion vector information comprises differential motion vector information for the set of pixels.

75. (Previously Presented) The method of claim 71 wherein the extended motion vector code is preceded in the bit stream by header information.

76. (Canceled)

77. (Canceled)

78. (Previously Presented) The method of claim 1 wherein the motion vector information represents plural values used to reconstruct a differential motion vector for the set of pixels.

79. (Previously Presented) The method of claim 1 wherein the set of pixels is a macroblock, and wherein the terminal symbol further indicates whether a coded block pattern code for the macroblock is included in the bit stream.

80. (Previously Presented) The method of claim 48 wherein the set of pixels is a macroblock, and wherein the decoding the macroblock further comprises:

if the terminal symbol indicates transform coefficient data for the macroblock is included in the bit stream, decoding a coded block pattern code for the macroblock, and

otherwise, skipping the decoding of the coded block pattern code for the macroblock, wherein the terminal symbol indicates whether the coded block pattern code for the macroblock is included in the bit stream.

81. (New) The computer system of claim 69 further comprising:
a display; and

one or more speakers;

wherein the communication connection is a wireless communication connection.

82. (New) A computing device that implements a video decoder, the computing device comprising:

one or more processing units;

memory;

a wireless communication connection;

a display; and

one or more speakers;

the computing device being adapted to perform a method comprising receiving encoded data from a bit stream and decoding a set of pixels using the encoded data from the bit stream, wherein the decoding the set of pixels comprises:

decoding an extended motion vector code for the set of pixels, wherein the extended motion vector code reflects joint encoding of motion information together with intra/inter decision information indicating whether the set of pixels is intra-coded or inter-coded and with a terminal symbol, wherein the extended motion vector code is a single instance of a single variable length code representing a combination of values for (a) the intra/inter decision information, (b) the motion information and (c) the terminal symbol, and wherein the decoding the extended motion vector code uses a variable length code table of different value combinations for the intra/inter decision information, the motion information and the terminal symbol; and

determining whether transform coefficient data for the set of pixels is included in the bit stream based at least in part upon the terminal symbol.

83. (New) The computing device of claim 82 further comprising a keyboard for accepting input from a user.

84. (New) The computing device of claim 82 further comprising a jitter buffer for storing the encoded data from the bit stream before decoding, a reconstructed frame buffer for storing reconstructed frames during decoding, and a playback buffer.